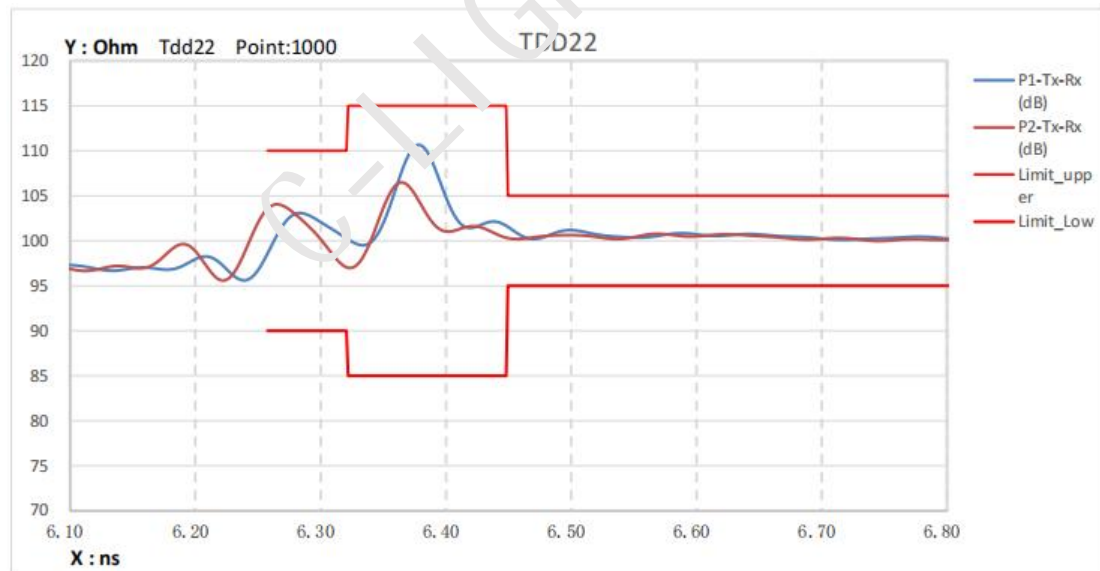
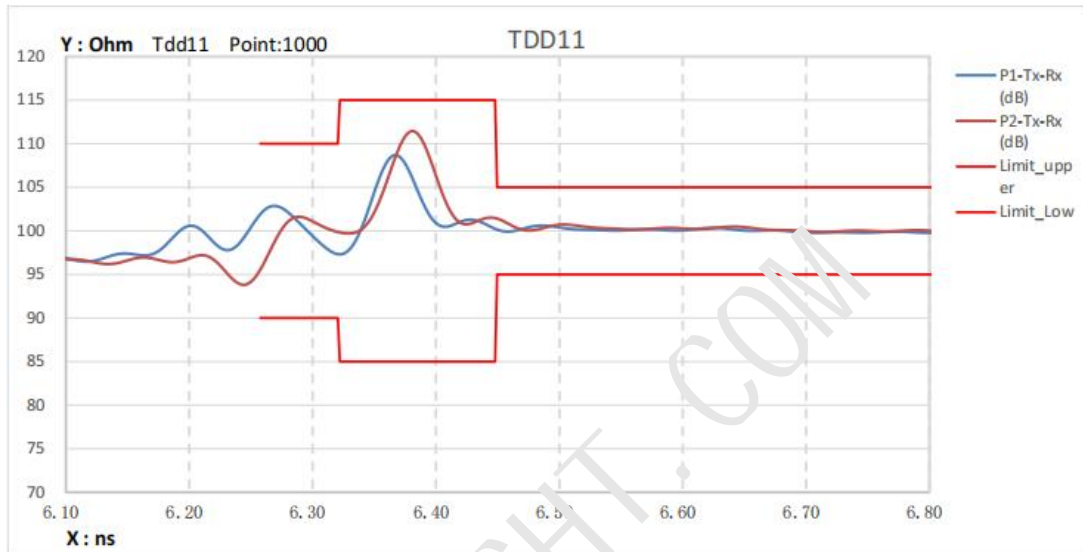


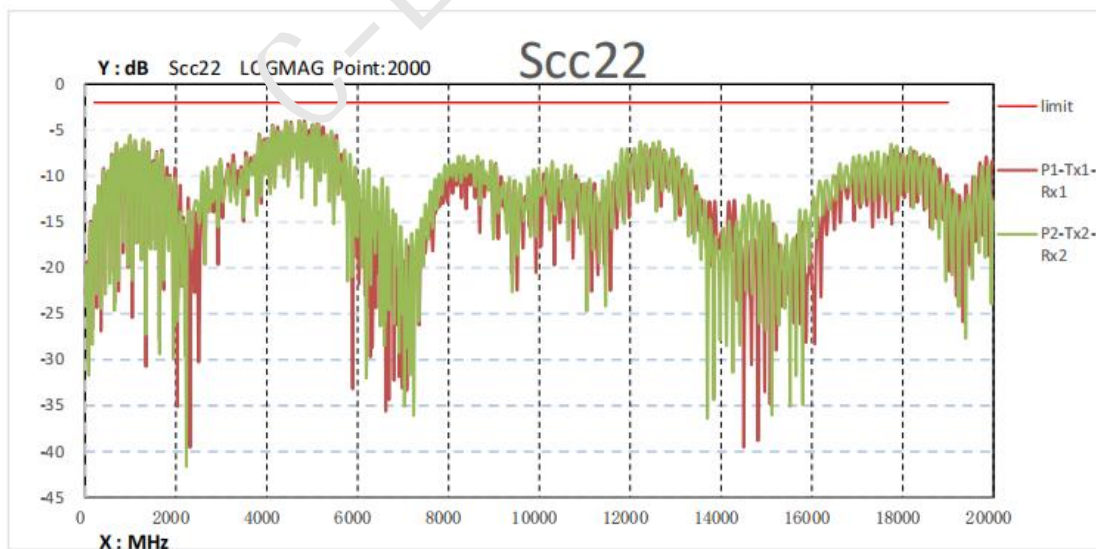
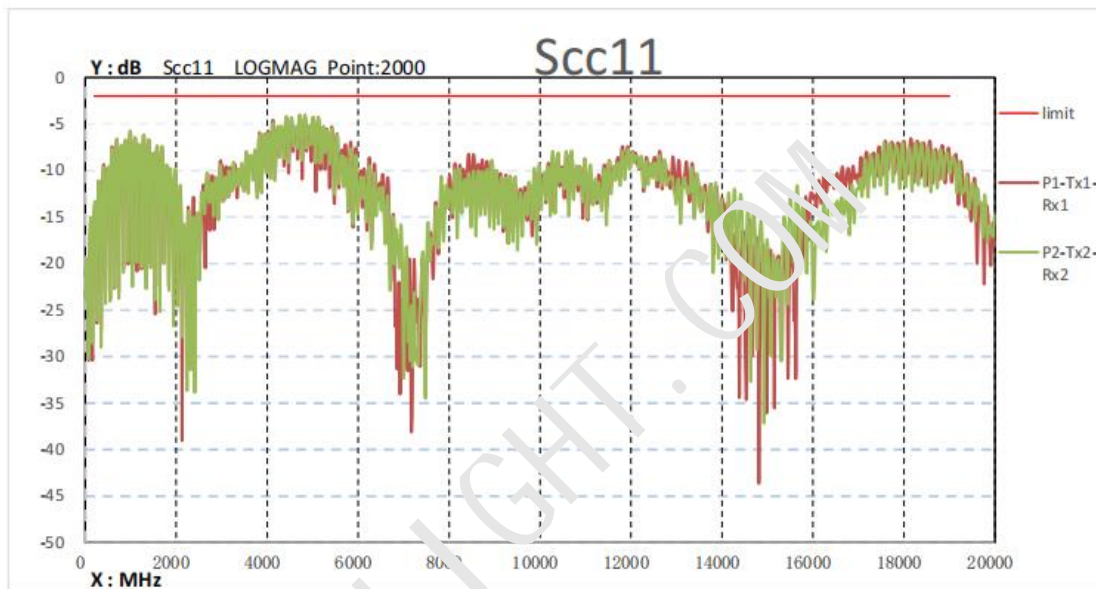
Attachment: S-parameters of CLC10GSFP-1M DAC (example)

1. Differential impedance (Tdd11/Tdd22) Rise-Time 25ps					
	ohm	Tdd11		Tdd22	
		Tx1	Rx1	Tx2	Rx2
Max	110	104.5	104.3	104.2	104.4
Min	90	96.5	96.3	96.4	96.7
Result		Pass	Pass	Pass	Pass



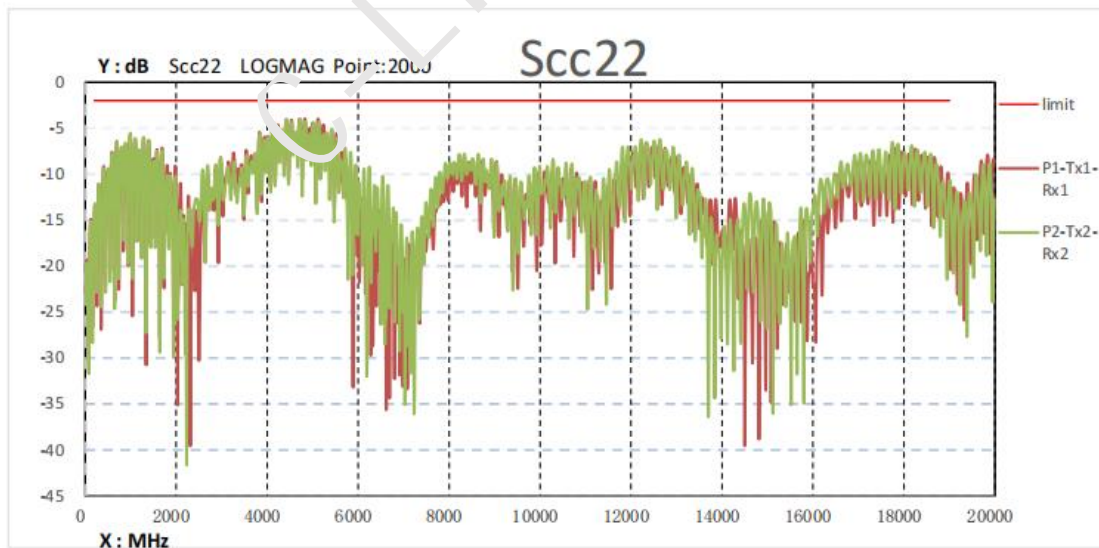
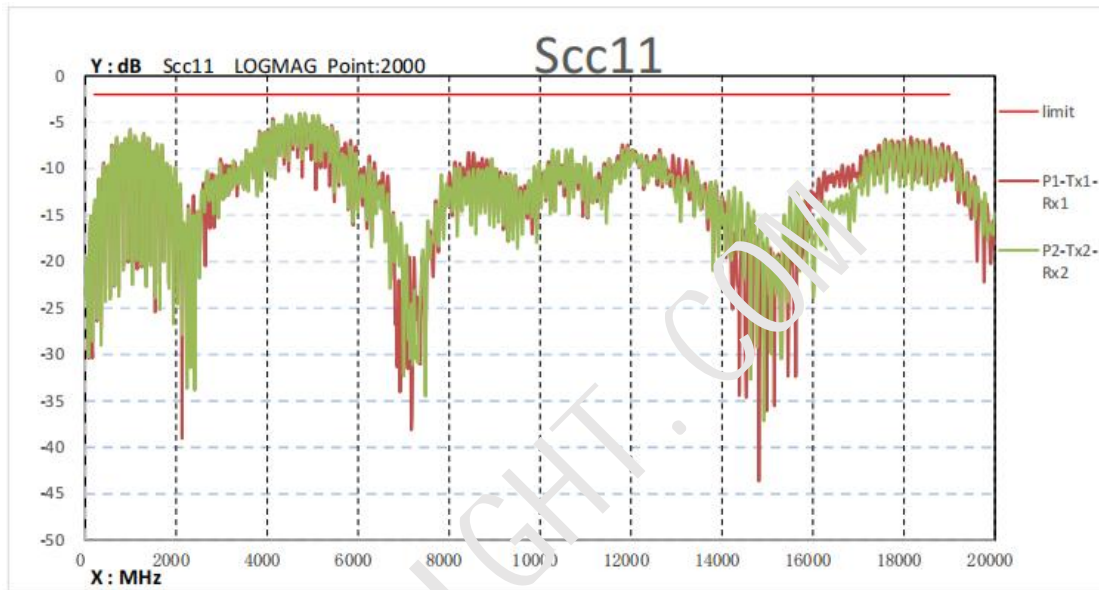
2. Differential return loss (Sdd11/Sdd22)

	Sdd11		Sdd22	
	Tx1	Rx1	Tx2	Rx2
Freq	10890	10880	3610	3610
Limit	-6.5	-6.5	-12.7	-12.7
Value	-12.7	-12	-17.6	-16.5
Margin	6.2	5.5	4.9	3.8
Result	Pass	Pass	Pass	Pass



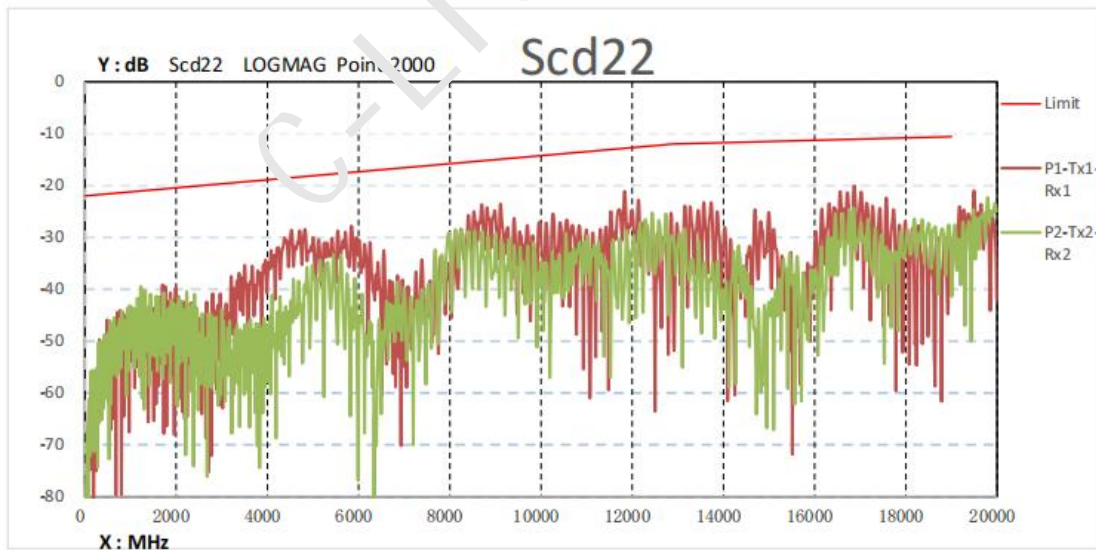
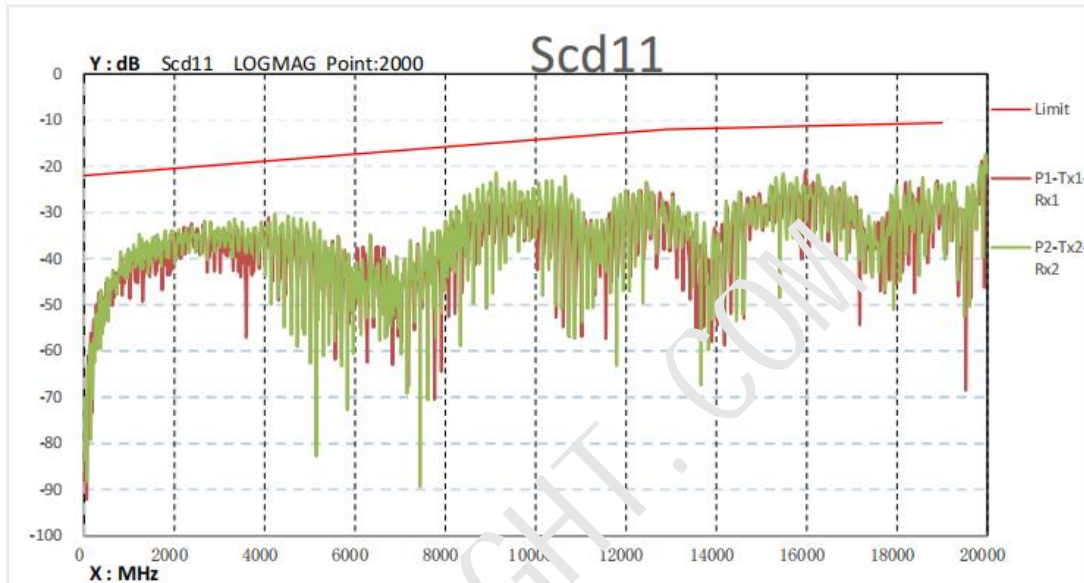
3.Common return loss(Scc11/Scc22)

	Scc11		Scc22	
	Tx1	Rx1	Tx2	Rx2
Freq	4700	4700	4700	4690
Limit	-2	-2	-2	-2
Value	-4.2	-4.2	-3.9	-3.7
Margin	2.2	1.9	1.9	1.7
Result	Pass	Pass	Pass	Pass



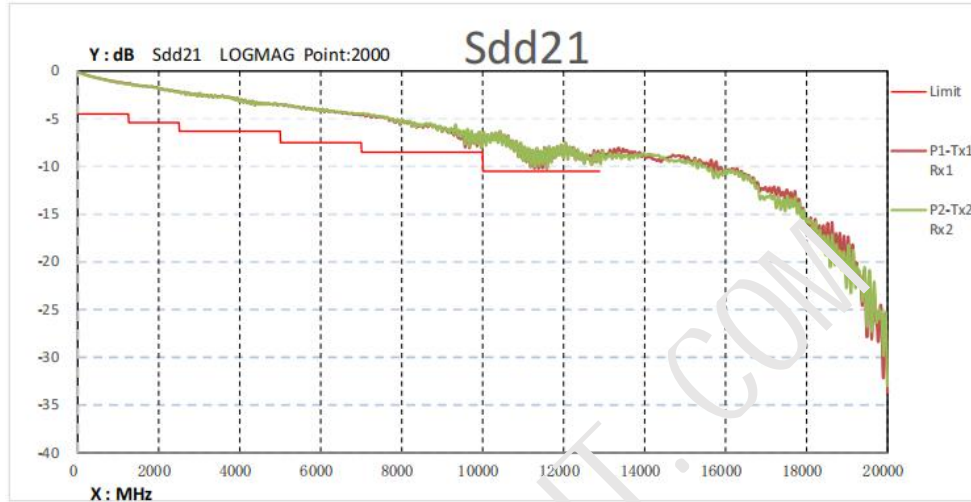
4. Mode conversion return loss (Scd11/Scd22)

	Scd11		Scd22	
	Tx1	Rx1	Tx2	Rx2
Freq	2400	9270	8550	8550
Limit	-20.1	-14.8	-15.4	-15.4
Value	-31.9	-17.7	-22.8	-22.8
Margin	11.8	2.9	7.4	7.4
Result	Pass	Pass	Pass	Pass



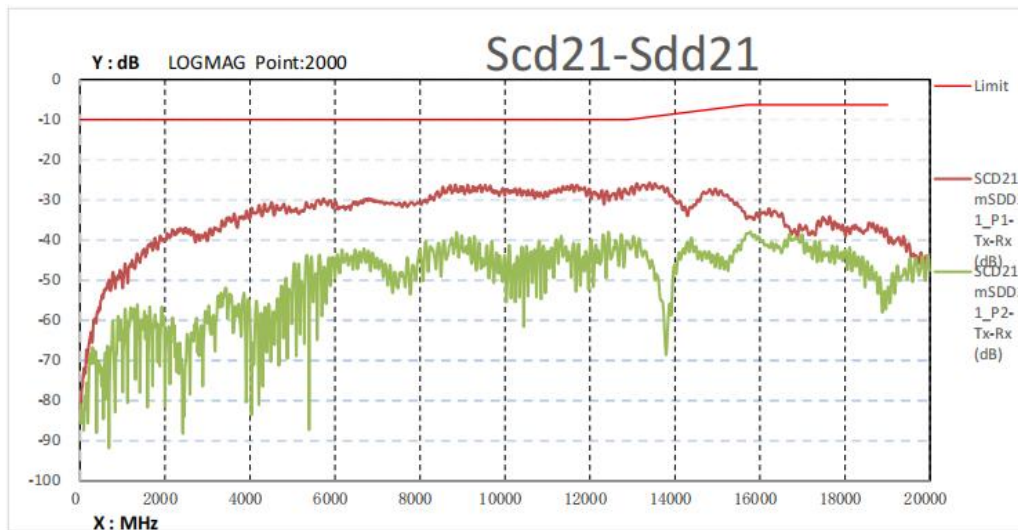
5.Insertion loss (Sdd21)

Sdd21		
	Tx1	Rx1
Freq	11590	11580
Limit	-10.5	-10.4
Value	-10.3	-10.4
Margin	0.2	0.1
Result	Pass	Pass



6.Mode conversion and insertion loss difference (Scd21-Sdd21)

Sdd21		
	Tx1	Rx1
Freq	5730	5630
Limit	-10	-10
Value	-29.7	-43.7
Margin	19.7	33.7
Result	Pass	Pass



7. Near end crosstalk (NEXT) dB

	Sdd21	
	L Tx1-Rx1	R-Tx1-Rx1
Freq	2470	11630
Limit	-26	-26
Value	-40.2	-35.4
Margin	14.2	9.4
Result	Pass	Pass

